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Transmission Date:

September 22, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Tu

Docket No.:

TSM03-0188

Serial No:

10/679,098

Art Unit:

2814

Date Filed:

October 3, 2003

Title:

Self-Aligned MIM Capacitor Process for Embedded DRAM

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- Election (1 page)

Respectfully submitted,

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Ngan Ngo

For:

Self-Aligned MIM Capacitor Process for Embedded DRAM

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Election

Dear Sir:

In response to Examiner's Restriction Requirement mailed September 16, 2004, Applicant herein elects Group I, claims 1-9 and 19, without traverse, for further prosecution.

Please contact Applicant's attorney with any questions regarding this matter.

Respectfully submitted,

Steven H. Slater Reg. No. 35,361

Attorney for Applicant

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